Docket Number: 53047/57370 **PATENT**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF E-FILING

I hereby certify that this correspondence has been e-filed with the U.S. Patent and Trademark Office on Sept. 1

Reg. No. 48,017

In re application of: Chamberlain et al.

Examiner: To be assigned Serial No.: 10/550,323

Filed: September 23, 2005 Group Art Unit: To be assigned

For: Intelligent Data Storage and

Processing Using FPGA Devices

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

TRANSMITTAL OF MISSING REQUIREMENTS

In response to the Notification of Missing Requirements Under 35 U.S.C. 371 in the United States Designated/Elected Office mailed on June 14, 2006, enclosed herewith for filing are the following:

- M A copy of the Notification of Missing Requirements.
- 冈 An oath or declaration in compliance with 37 CFR 1.497(a) and (b), including residence information and identifying the application by the above Application Number and Filing Date.
- \boxtimes A Power of Attorney or Authorization of Agent.
- \boxtimes Statement Under 37 CFR 3.73(b) with a copy of the Assignment.

The Director is hereby authorized to charge Deposit Account No. <u>20-0823</u> for the additional fee of <u>\$125.00</u>. The Commissioner for Patents is authorized to charge any deficiencies to Deposit Account <u>20-0823</u>. A duplicate copy of this Petition is enclosed for that purpose.

Please file said Missing Requirements.

Respectfully submitted,

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